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Complete if Known

Application Number	09/837,897
Filing Date	April 18, 2001
First Named Inventor	Allott, Stephen
Group Art Unit	2817
Examiner Name	Unassigned
Attorney Docket Number	020408-002000US

(use as many sheets as necessary)

Sheet	1	of	2
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[illegible][illegible]Date
Considered

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Substitute for form 1449B/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>JS</i>	1	JOHNS, DAVID A, and MARTIN, KEN, "CMOS Transconductors Using Triode Transistors," <u>Analog Integrated Circuit Design</u> , John Wiley & Sons, Inc., New York, 1997, pp 597-600.	<input type="checkbox"/>
<i>JS</i>	2	TSIVIDIS, YANNIS P., "Model Accuracy and Parameter Extraction," <u>Operation and Modeling of the MOS Transistor</u> , McGraw-Hill, 1987, pp. 154-160.	<input type="checkbox"/>
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